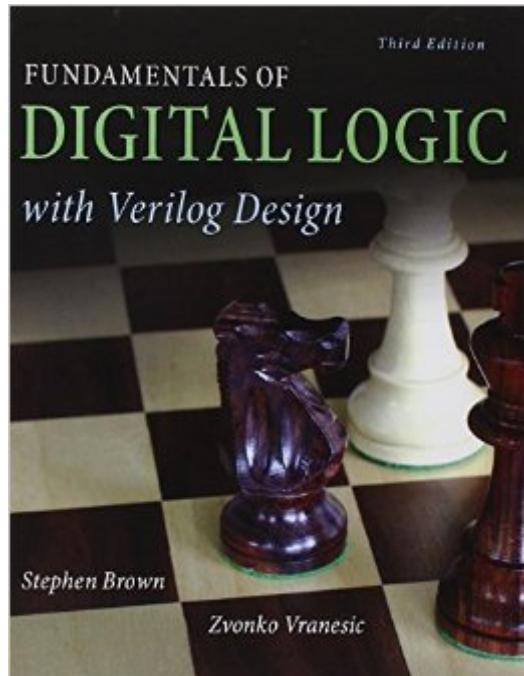


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# Fundamentals Of Digital Logic With Verilog Design



## **Synopsis**

Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD software is well integrated into the book. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the CAD systemcompile the design into a selected devicesimulate the functionality and timing of the resulting circuitimplement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

## **Book Information**

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## **Customer Reviews**

The publisher of this book made no apparent effort to place illustrations on the same page as the text referring to them. The text consistently refers to illustrations and examples several pages forward or back. I find this completely unacceptable for a text of this type, at this expense, and in third edition. Much time spent reading this text is wasted looking for the page with the illustration or

example. If they did not want to take the time to format correctly, they should have simply printed a companion with illustrations and designs contained separately.

Good textbook, but not as concise as it could be on many of the topics. The kindle version is inconvenient because the pictures and diagrams are almost never on the page discussing them, and are sometimes separated by two or more pages. While flipping back in forth in a hard copy textbook wouldn't be too annoying, constantly flipping back and forth in the Kindle version gets old. Same thing for flipping between the problems at the end of the chapter, the example problems, and the explanations of the material. Highly recommend getting hard copy over Kindle Version.

Coming from a course where the instructor was less than helpful, this book did a good job filling in the gaps (and there were a lot of them) in the class instruction. The class only covered the first 6 chapters so I can't speak for the rest, but chapters 1-5 were great, 6 was a bit tougher to handle but I think that's just the nature of the material. Also can't speak to the Verilog portions as we didn't cover that either. All in all a good book.

I have a 2 year certificate from an electronics trade school and I wanted to brush up on my digital logic so I could build a simple project. After reading several chapters of this book I found it to be way over my head and I think it was written for people with a much stronger background in digital logic and not for the beginner.

The content is the same as the 2nd ed., but homework is different. So if you are asked to buy the 2nd ed., you can use this text but make sure you have access to the 2nd to do homework.

The book is "wide" in the sense it covers a lot of topics. But it's not "deep" ; only an introductory level treatment, which is not good. No explanations regarding the design procedure. I got this because it was preferred by the instructor, and was very disappointed.

The appendix is the best part, some stuff is a bit over complicated to solve based on the content, especially when used in a sophomore level course. Good figures

It's a pretty good book. The more I study it, the more I like it. It's pretty easy to follow and there are a lot of good Verilog examples.

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